Doc code IDS

PTO/SB/08a (09-08)
Approved for use through 10/31/2008 OMB 0651-0031
U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE Doc description: Information Disclosure Statement (IDS) Filed Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

NFORMATION DISCLOSURE STATEMENT BY APPLICANT Not for submission under 37 CFR 1.99)	Application Number		10544894		
	Filing Date		2006-07-19		
	First Named Inventor	Dasu,	, Aravind R.		
	Art Unit		2193		
	Examiner Name	Bullock Jr., Lewis Alexander			
	Attorney Docket Numb	er	117316-155055		

U.S.PATENTS Remove											
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue D	late	Name of Patentee or Applicant			ges,Columns,Lines where evant Passages or Relevan ures Appear		
	1										
If you wish to add additional U.S. Patent citation information please click the Add button. Add											
U.S.PATENT APPLICATION PUBLICATIONS Remove											
Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publica Date	tion	Name of Patentee or Applicant Re			Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear		
	1										
If you wisl	h to a	d additional U.S. Publis	hed Ap	plication	citation	n information p	lease click the Ad	d button	Add		
				FOREIG	SN PAT	ENT DOCUM	ENTS		Remove		
Examiner Initial*	Cite No		Country Kind Code ⁴		Publication Date	Name of Patentee or Applicant of cited Document		Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear		т.	
	1										
If you wish to add additional Foreign Patent Document citation information please click the Add button Add											
NON-PATENT LITERATURE DOCUMENTS Remove											
Examiner Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (look, magazine, journal, serial, symposium, catalog, etc.), date, pages(s), volume-issue number(s), publisher, city and/or country where published.							Τs				

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

Application Number Filing Date			10544894
			2006-07-19
	First Named Inventor	Dasu,	Aravind R.
	Art Unit		2193
	Examiner Name	Bulloo	ok Jr., Lewis Alexander
Attorney Docket Number		er	117316-155055

1	Singh, H. et al. "MorphoSys: An Integrated Reconfigurable System for Data-Paratlel Computation-Intensive Applications" IEEE Transactions on Computers, 2000	
2	Soukup, J. "Fast Maze Router" Proceedings of the 15th Design Automation Conference, pp. 100-102, 1978	
3	Strauss, M., et al. "BOPS: Conquering the Geometry Pipeline" Game Developers Conference, March 22-26, 2004, San Jose, California	
4	Tayor, Michael Bedford, "Design Decisions in the Implementation of a Raw Architecture Workstation", MS thesis, MIT, 1996	
5	Taylor, R. et al." A High-Performance Flexible Architecture for Chyptography" CHES'99, LNCS 1717, pp. 231-245, 1998	
6	Vorbach, M., Becker, J. * Reconfigurable Processor Architectures for Mobile Phones* Proc of International on Parallel and Distributed Processing Symposium, 2003	
7	Wan, M. et al. "Design Methodology of a Low-Energy Reconfigurable Single-Chip DSP System" Journal of VLSI Signal Processing Systems, 28, pp. 47-51, May-June 2001	
8	Wan, M. et at. * Design Methodology of a Low-Energy Reconfigurable Single-Chip DSP System* Journal of VLSI Signal Processing, 2000	
9	Wolf, W., et al. * The Princeton University Behavioral Synthesis System* Proc. Design Automation Conf., June, 1992, pp. 162-167	
10	Yamauchi, T. et al. " SOP: A Reconfigurable Massively Parallel System and Its Control-Data-Flow based Compiling Method! IEEE Symposium on FPGAs for Custom Computing Machines, 1996	
11	Ye, Z. et al. "CHIMAERA: A High-Performance Architecture with a Tightly-Coupled Reconfigurable Functional Unit" International Conference on Computer Architecture, 2000	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10544894		
	Filing Date		2006-07-19		
	First Named Inventor	Dasu,	Aravind R.		
	Art Unit		2193		
	Examiner Name Bulloo		ook Jr., Lewis Alexander		
	Attorney Docket Numb	er	117316-155055		

	12	Zhang, H., et al. * A 1-V Heterogeneous Reconfigurable DSP IC for Wireless Baseband Digital Signal Processing* IEEE Journal of Solid-State Circuits, 35 (11), pp. 1697-1704, November 2000				
	13	Zhang, H., et al. * Interconnect Architecture Exploration for Low-Energy Reconfigurable Single-Chip DSPs* IEEE Computer Society Workshop on VLST99 pp. 2-8, April 1999				
Market and the second s						

If you wish to add additional non-patent literature document citation information please click the Add button Add

EXAMINER SIGNATURE

Examiner Signature

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Table Not Code of 18710 Pastin Documents it your USETIO.001 on NETS 961.04. There often between the document by the her-befor code (NPD) Second of 13. If Ye begander plant documents, the included of the year of the night of the Empour must proced by sent or under of the process. The Empour must proced by sent or pastin document of "folior of bocument by the appropriate symptoms as advanted on the document under NPD Standard ST.16 (spoads - "Applicant the boards careful must be not proceed to the pastin document them for the pastin advantage of the pas